



ST. FRANCIS INSTITUTE OF TECHNOLOGY (Engineering College)

MOUNT POINSUR, S.V.P. ROAD, BORIVLI (WEST), MUMBAI 400103

(Roman Catholic Minority Educational Institute)

DTE Code: EN3204

www.sfitengg.org

Approved by AICTE | ISO 9001:2015 Certified
Permanently Affiliated to University of Mumbai
All U.G. Programs are NBA Accredited

Courses at SFIT

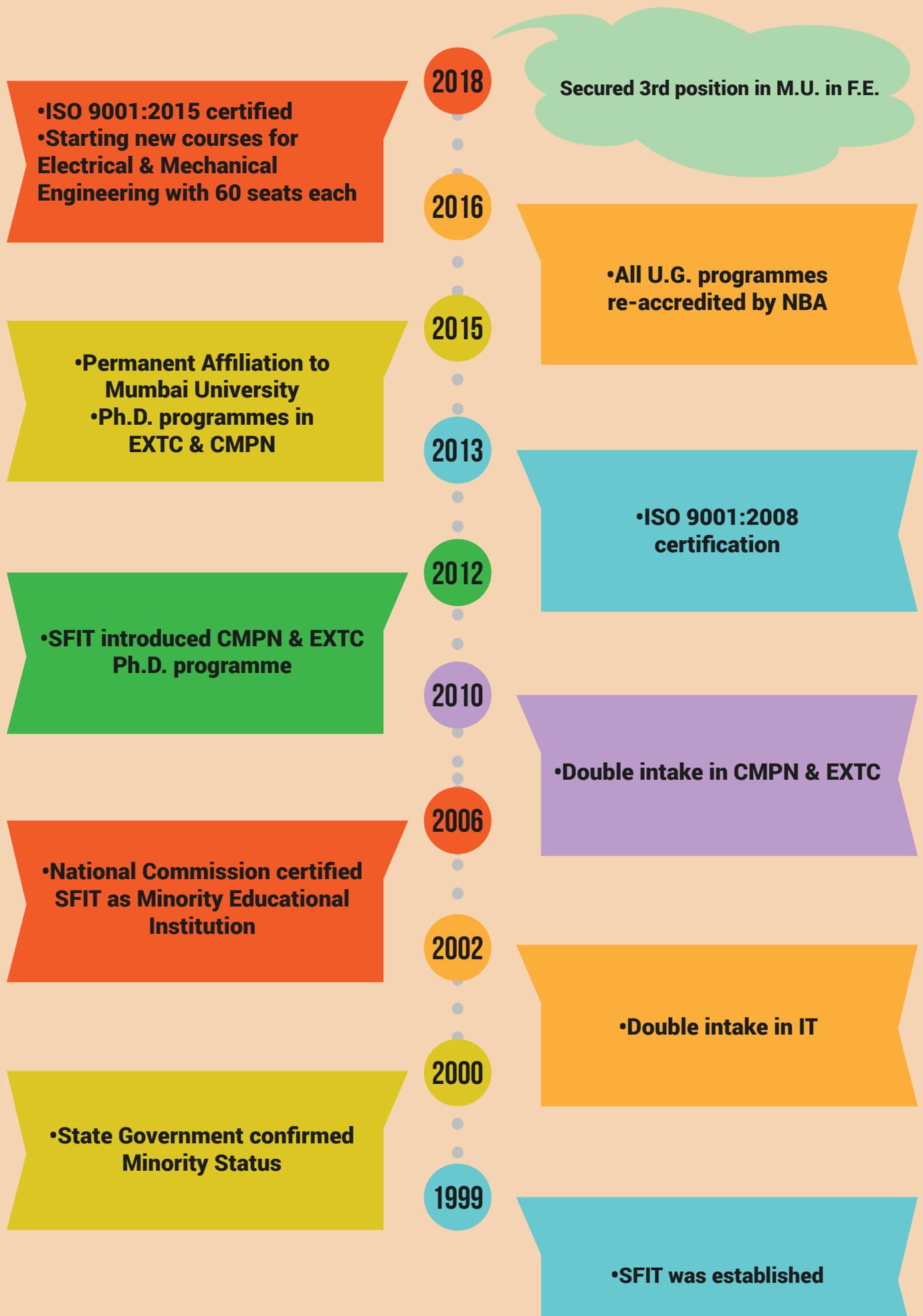
- **Computer Engg. [U.G., P.G. & Ph.D.]**
- **Electronics & Telecommunication Engg. [U.G., P.G. & Ph.D.]**
- **Information Technology [U.G.]**
- **Mechanical Engineering [U.G.]**
- **Electrical Engineering [U.G.]**



PROSPECTUS

DEVELOPING ENTREPRENEURS/INDUSTRY LEADERS WITH INTEGRITY

Road Map of SFIT



Society and Institute

St. Francis Institute of Technology (SFIT) was established in 1999 with a view to create an engineering institute to fulfill the long-felt need for an institute of excellence in Borivli. It is run by “The Society of the Congregation of Franciscan Brothers”, an international society of Religious Brothers founded by Bro. Paulus Moritz in 1901 and registered as a Public Charitable Trust.

Our Vision

To be a chrysalis where bright youngsters are transformed into technological entrepreneurs and innovative leaders of tomorrow's world, consistent with the Franciscan vision of integrity, peace and love.

Our Mission

To churn highly competent engineering graduates with a commitment to result-oriented work, a perennial zest for learning, a quest for excellence, an open mind and the universal values of honesty, dignity and mutual care.

Academic Programmes/Courses Offered

Undergraduate (U.G.) Level

1. Bachelor of Engineering : Computers - 120 seats
2. Bachelor of Engineering : Electronics and Telecommunication - 120 seats
3. Bachelor of Engineering : Information Technology - 120 seats
4. Bachelor of Engineering : Mechanical - 60 seats (NEW)
5. Bachelor of Engineering : Electrical - 60 seats (NEW)

Postgraduate (P.G.) Level

1. Master of Engineering : Computers - 18 seats
2. Master of Engineering : Electronics and Telecommunication - 18 seats

Ph.D. (Technology) Level

1. Doctor of Philosophy (Ph.D) : Computers - 10 seats
2. Doctor of Philosophy (Ph.D) : Electronics and Telecommunication - 10 seats

Director's Message



My young friends at the very outset I am happy to invite each one of you to SFIT. It was in the year 1999 that the St. Francis Institute of Technology was established with the motto of providing quality professional education at affordable cost and the vision of developing entrepreneurs/industry leaders with integrity. As of today, it is gratifying to note that the institute has lived up to its expectation by being one of the top three colleges of University of

Mumbai. On the placement front as well, the institute is doing extremely well.

Tony Wagner, the author of "Creating innovators" says that schools and universities are failing to spark young people's curiosity. In SFIT, faculties in collaboration with the management are trying their level best to make the teaching learning process more skill oriented and interesting. With my long stint of little more than three decades in the field of education, I have come to realize that universities and industries need to come together to create synergies. This will enhance the practical knowledge of the students and they will be in a better position to start their career. In fact, SFIT has proactively initiated various plans for student's employability. The main objective of higher education in India today is to make global citizens. Our students are ready to step out of their comfort zone and move ahead to follow their dreams. Our placement faculty is helping them to take their first step.

Once a poor and uneducated man dreamt of a horseless carrier. He went to work with what tools he possessed, without waiting for opportunity to favor him, and now evidence of his dream is felt in the entire earth. That man, Henry Ford, has put more wheels into operation than any man who has ever lived, because he was not afraid to back his dream.

What we Indians need is courage. My young friends, I would like to tell you not to lose hope and courage. Success can only come to you by courageous devotion. I can assert without fear of contradiction that the quality of the Indian mind is equal to the quality of any European, American, Russian, Chinese or Japanese mind. What we lack perhaps the driving force, which takes one anywhere. What we need in India today is the belief in our own potential and the spirit of a winner. If that indomitable spirit arises, nothing can hold us from achieving our destiny.

Technology has come to play a dominant role in improving the quality of life. It is the engine capable of driving a nation towards growth and prosperity and giving it the necessary competitive edge in the comity of nations. Let us make use of this technology innovatively to transform India into a developed country.

Bro. Jose Thuruthiyil
Director, SFIT

From the Principal's Desk...



Let me begin by extending a warm welcome to the youth to whom the future belongs. Dr. APJ Abdul Kalam rightly said, "Excellence is a continuous process, not an accident." St. Francis Institute of Technology has come a long way with quality education imbued with professional and ethical values. We have always emphasized the importance of values and social responsibilities along with professional competence and research aptitude. The current scenario at local,

national and international level is of intense competition with equal opportunity to all. It is hence essential to provide our students with maximum international exposure to prepare them to face global challenges. SFIT has been organizing a triennial international conference since 2014. In 2017-18, we received and selected quality papers from 7 countries, namely, India, USA, Canada, Switzerland, Egypt, China, Australia and Thailand. These papers were published in IEEE Xplore. We have annual seminars for students wherein they get to interact with experts from International Universities. The experts from the Harrisburg University, USA conducted a seminar for our students and 7 students from SFIT received the opportunity to attend a credit course on Artificial Intelligence at Harrisburg University during June 15 to July 15, 2018. Our students from RADLAB, represented SFIT at the competition, Makers Fair in Paris in 2014. Efforts of our students were recognised at the first Amazon Picking Challenge (APC) held at the 2015 International Conference on Robotics and Automation in Seattle, Washington. We provide our students with the best of exposure and learning opportunities to develop them into holistic, responsible professionals who can contribute not only to the technological demands of the world but also the betterment of society. Welcome to a warm academic community.

Dr. Sincy George

Principal, SFIT

Highlights at SFIT

- U.G., P.G. and Ph.D. programmes offered
- Among top 3 Engineering colleges in University of Mumbai
- Above 70% First year result for 2017-18
- Above 96% Final year result for 2016-17 with IT Dept. securing 100% result
- Regular Training programmes for Faculty Development
- Minor Research Grants regularly received from Mumbai University
- Major Research Grant received by faculty from Board of Research in Nuclear Sciences
- Seminars from Industrial experts for active interaction with students
- Summer workshops/seminars for First year students
- Certification Courses for students in Higher semesters
- Students encouraged to take up internships during vacations (India and overseas)
- An effective mentoring system for students
- Full time Counsellor to guide students when needed
- Industrial visits for maximum industrial exposure
- Active participation of students in inter-college events.

Department of Computer Engineering

HOD's Message



Department of Computer Engineering has enthusiastic team of staff and students for co-ordination and execution of the Technical activities. Our Students associations, CODE-X and student CSI chapter play an important part in the same. CODE-X is solely of the student, for the student and by the student. Being the heart of our institute, we are

giving strength, motivation, ethical values to our young aspiring engineers besides technical and professional skill sets. We always encourage our students to be socially aware, responsible, committed, so they can become smart citizens of today's era. In line with our vision, we are constantly striving hard to improve quality and remain committed to focus on excellence at par with global standards. I welcome you to join our endeavors to deliver and bring out the best in our students at SFIT.

Dr. Kavita Sonawane

HOD - CMPN Dept., SFIT

Department Highlights

- 98.65% result in the B.E. examination held in May/June 2017
- Participation of more than 18 students in Hackathon 2017 and 2018
- Research papers by students in reputed conferences like IEEE/Stringer/Elsevier/ACM
- Computer Student association, 'CODE-X' - a platform to execute technical dreams
- 5 faculties have received Minor Research Grants from University of Mumbai
- AICTE-ISTE approval received for organising a short-term training programme, 'TRANSFORMATIONAL LADDER TO DIGITIZE INDIA'
- Highly qualified faculty with a passion for teaching.
- Well established teaching learning process and well-defined testing and evaluation system to ensure excellent academic performance.

Research Guides/Scholars

Name of Faculty	Designation	Area of Specialisation	Teaching/ Industry Experience
Dr. Kavita Sonawane	Professor and HOD	Image Processing and Analysis	17
Ms. Bidisha Roy (Pursuing Ph. D.)	Associate Professor	Computational Intelligence	17
Ms. Dakshata Panchal (Pursuing Ph. D.)	Assistant Professor	Computer Graphics	15
Ms. Shobha Tyagi (Pursuing Ph. D.)	Assistant Professor	Mobile Adhoc Networks	12

Total faculties: 24

Professor: 1

Associate Professor: 3

Assistant Professor: 20

Faculties with more than 15 years' experience: 4

Staff



Laboratories:

The department has 08 computer labs like Operating Systems and Networks Lab, Software Engineering Lab, Database Lab, etc., 02 project labs, and 01 research and development lab, all of which are well furnished with high-end equipment.

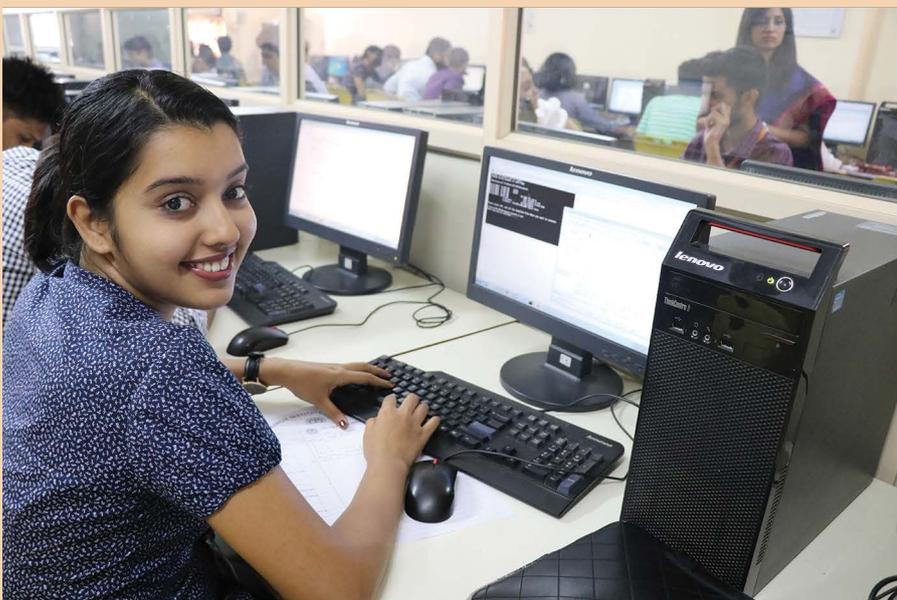


Operating Systems and Networks Lab.

Operating Systems and Computer Networks laboratory with JAVA, TASM, PROLOG, Fedora Linux, NS2 Simulator and many other softwares available to help students understand the basic operating systems and computer networks.

Computer Graphics and Advanced Microprocessor Lab.

Basics of Computer Graphics and Microprocessor concepts are inculcated to the students using the high-end configuration hardware in the laboratory and for computer graphics JDK 1.8, Net Beans, TASM, Quincy and other softwares are available.



Database Lab.

Students work with database related applications such as Oracle, SQL, Postgres SQL and other softwares available in the laboratory. Development of database applications can be most efficiently done here.

Software Engineering Lab.

Designing and analysis using Rational Rose, which is pre-installed on every system along with the basic programming languages. Here the students are trained at the initial stage of Designing software projects.



Image Processing and Soft Computing Lab.

Understanding the basic concepts of Image processing and Soft Computing using open source tools like scilab, octave and other basic programming languages available in the laboratory.

Computer Programming Lab.

Basic computer programming languages including JAVA, C, C++ and others are taught here. It helps students to strengthen the basics of programming languages. Programming skills are nurtured in this laboratory.



Department of Electronics and Telecommunication

HOD's Message



Over the years, we have been striving hard pursuing our vision to become a centre of excellence, by upgrading the qualifications of the faculty, promoting research and developing the infrastructure. Slowly but steadily we have grown by increasing the intake for the undergraduate Bachelors programme from 60 in 1999 to 120 in 2010, introducing the postgraduate Masters programme in 2012 and Doctoral programme in 2015.

We are updating our knowledge by attending workshops, training programs conducted by premier institutes like IITs, NITs and IISCs. Our institute is a remote centre for programmes conducted by IITs under the National Mission on Education through ICT. We are presenting and publishing our research work in national/international conferences/ journals like NCC, INDICON, TENCON, Elsevier and Springer. We have received research grants from and are involved in setting up of syllabus for the Electronics & Telecommunication branch in the University of Mumbai.

It has always been our endeavour to nurture and develop graduates and post graduates not only with a strong academic foundation, but also with an all-round personality and ethical values. On the academic front, our results are consistent and quite impressive. Students of our department have achieved top university ranks. They are very dynamic and involved in co-curricular & extra-curricular activities. They are associated with Robotics and automation development laboratory (RadLab), participate in the events like Robocon a National Robotics Competition, & E-yantra, an initiative by IIT Bombay. On the social front, they are involved in the activities organized by the NSS Cell of the institute.

Dr. Gautam Shah

HOD - EXTC Dept., SFIT

Department Highlights

- In B.E. examination held in May/June 2017, the result was 94.84%
- 3 faculties have received Minor research grants from University of Mumbai
- AICTE-ISTE approval received for organizing a short-term training program on Commercial Trends in Antennas and Microwave Technologies
- 5 faculties are Ph.D. approved guides from University of Mumbai
- Received best paper award for research paper titled "Compact wideband quarter-wave transformer using stepped impedance microstrip lines" in 2nd International IEEE Conference on Communication Systems, Computing and IT Applications (CSCITA), 2017
- A student secured 1st place in IMC Inclusive Innovation Awards 2017
- A student received 1st place in "PRAGATI" Project Competition at SFIT
- A student received MRS. MANORAMA RATHORE MEMORIAL AWARD for scoring highest marks in the subject Circuits & Transmission Line, which was held in Dec-2017 examination

Research Guides/Scholars

Name of Faculty	Designation	Area of Specialisation	Teaching/ Industry Experience
Dr. Uday Pandit	Professor	Electronic Systems	25
Dr. Deepak Jayaswal	Professor	Signal Processing	27
Dr. Gautam Shah	Professor and HOD	Analog Signal Processing	26
Dr. Kevin Noronha	Professor	Medical Image Processing	18
Dr. Ravindra Chaudhari	Associate Professor	VLSI and Signal Processing	16
Ms. Jayasudha Koti (Pursuing Ph. D.)	Associate Professor	Optical Wireless Communication	18
Ms. Anjali Chaudhari (Pursuing Ph. D.)	Assistant Professor	Antenna Design	11
Mr. Vaqar Ansari (Pursuing Ph. D.)	Assistant Professor	Signal Processing	13
Mr. Santosh Chapaneri (Pursuing Ph. D.)	Assistant Professor	Signal Processing	18

Total faculties: 24

Professor: 4

Associate Professor: 2

Assistant Professor: 18

Faculties with more than 15 years' experience: 8

Faculties with industry experience: 5

Staff



Laboratories:

The department has 09 electronics labs like Analog & Digital Electronics Lab, Communication Systems Lab, Basic Electronic Lab., etc. and 01 research lab, all of which are well furnished with high-end tools.

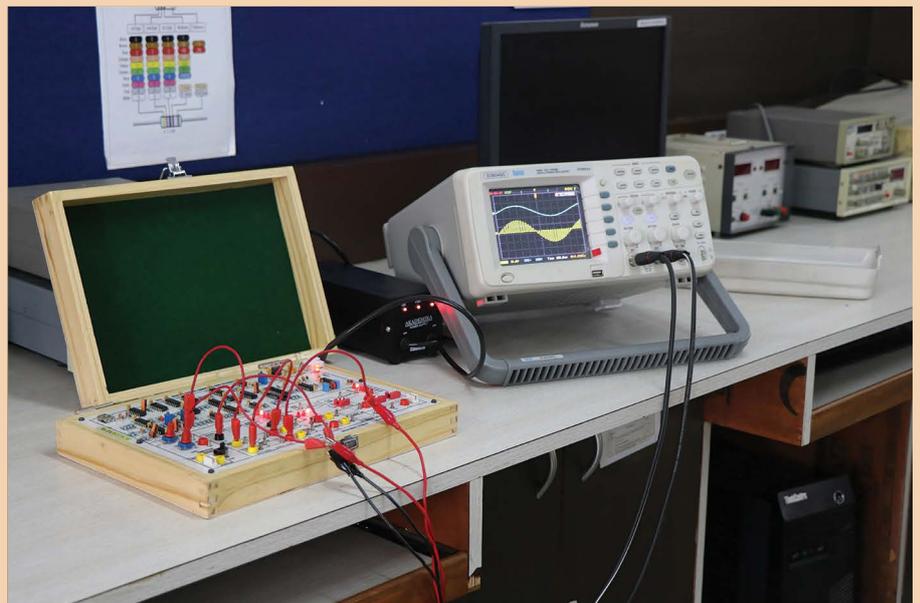


Analog & Digital Electronics Lab.

To analyze and design circuits and systems for particular application of linear integrated circuits. The lab is equipped with ICs, Digital Storage Oscilloscopes, Function Generators and Power Supply, along with open source simulation tools.

Basic Electronic Lab.

To understand the working of semiconductor devices and design and operation of amplifier/ oscillator circuits. The lab is equipped with Digital Storage Oscilloscopes, Function Generators and Power Supply along with Simulation Software like LT Spice, OSCAD, Circuit Maker, Express PCB.



Basic Electrical and Electronics Lab.

To understand the fundamentals of dc and ac circuits and its applications. The lab is equipped with CRO, Function Generator, Power Supply, Varlac-3 Phase Capacity, 3-Phase Lamp Load.

Communication Systems Lab.

To understand the basics of modern communication technology and systems. The lab is equipped with CISCO Packet Tracer, Fiber Optic kit, Analog/ Digital Communication kit, WireShark, Satellite Trainer.



Microwave and Antenna Lab.

To understand the working of microwave devices and verify the working of designed antenna/ microwave systems. The lab is equipped with Microwave Bench, Antenna Trainer Kit, Vector Network Analyzer.

Signal Processing Lab.

Develop fundamental and mathematical models in signal processing for real-world applications. The basics of image and video processing as well as digital signal processing are introduced using open source simulation tools and TMS kits.



Department of Information Technology

HOD's Message



The progression of Information and Communication Technology is outpacing the expectations of humankind. Proliferation in the developments of Applications and Robotics is astounding. E-Commerce is flourishing and growing at an astonishing rate. As a result, the academic institutes in the country are facing the challenge to produce

qualitative engineers capable of delivering effective solutions in the above areas. Taking into account the needs of the industry and our institute's constant endeavor to produce entrepreneurs and employable IT engineers of high quality, the department continues to play an active role to achieve these objectives. In view of the above developments, the Information Technology Department imparts training to students through its well-equipped laboratories and infrastructure to make them competent to be able to devise innovative ideas to serve the society. In addition to imparting technical knowledge, the department also focuses on inculcating ethical values in students.

Since its inception the IT Department holds the record of being one of the best Information Technology Departments in the University of Mumbai.

Dr. Joanne Gomes

HOD - INFT Dept., SFIT

Department Highlights

- 100% result in BE examination held in May/June 2017
- 10 successful Startups by our students in a span of 5 years
- Minor research grants received from University of Mumbai
- AICTE grant for conducting seminar
- Annual Colloquium, "IT for Society"
- Highly qualified faculty with a passion for teaching.
- Well established teaching learning process and well-defined testing and evaluation system to ensure excellent academic performance.

Research Guides/Scholars

Name of Faculty	Designation	Area of Specialisation	Teaching/ Industry Experience
Dr. Joanne Gomes	Professor and HOD	Embedded Systems, IOT, Wireless Networks, Data Mining and Power Electronics	29
Dr. Prachi Raut	Associate Professor	Nano-Technology, Wireless Networks, Simulation and Modelling	16
Dr. Vaishali Jadhav	Assistant Professor	Parallel & Distributed Computing and Big Data Analytics	13
Ms. Nazneen Ansari (Pursuing Ph. D.)	Associate Professor	Big Data Analytics and Data Mining	19
Ms. Nitika Rai (Pursuing Ph. D.)	Associate Professor	Wireless Communication, Antenna Design and Participatory sensing	19
Ms. Minal Lopes (Pursuing Ph. D.)	Assistant Professor	Image Processing and Information Security	15

Total faculties: 24

Professor: 1

Associate Professor: 3

Assistant Professor: 20

Faculties with more than 15 years' experience: 4

Staff



Laboratories:

The department has 09 labs like Computer Graphics Lab, Database Lab, Computer Design Lab, etc. and 02 project labs, all of which are well furnished with sophisticated tools.



Computer Graphics Lab.

Fundamental Knowledge of Computer Graphics and its applications are explored in this lab. Latest technologies and open source technology like OpebGL are used for demonstration.

Computer Design Lab.

This lab provides students with hand-on experience of installation of different software and operating systems such as Windows, Linux etc.



Image Processing Lab.

Image processing lab with Matlab and Octave on Ubuntu platform helps students to practice image processing tools for enhancement, compression and storage of Images.

Database Lab.

DBMS lab aims at practicing and achieving automated system that manages, modifies and update data accurately by using various software's such as sql, oracle, and MS – access etc.



Computer Networking Lab.

The networking lab includes courses on computer networks and wireless communication. Students get to implement networking protocols and learn different network traffic analysis tools. The softwares used in this lab include Matlab, Scilab, Octave and NS-2.

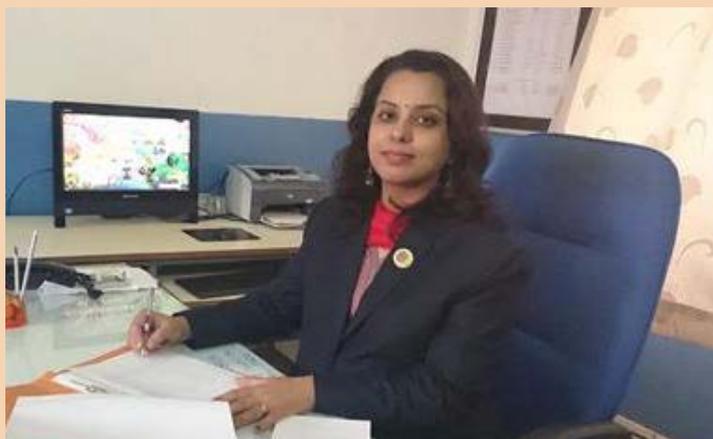
Computer Software Lab.

Advanced internet technology can be well experienced here. Students create their websites and host them; efficient storage mechanism, techniques to represent data in real world are taught here. JDK7, JRE7, MS-Project are some of the required s/w available here.



Department of Basic Science and Humanities

HOD's Message



The Department of Basic Science and Humanities started from inception of the college. It inculcates basic knowledge in Mathematics, Physics, Chemistry, Environment Science and Mechanics, which is the foundation of Engineering education and in Communication skills that helps students to express themselves effectively. The Science and Humanities departments is one of the core departments, which strives to deliver quality

based practical knowledge to the students. Department is adorned with adequate, well-experienced faculty with specialization in diverse fields to impart knowledge and enhance skills of our students. Dedicated faculty members of the department have made consistent and strenuous efforts to improve the students learning and development processes. To support the theoretical aspects of the subjects and impart practical knowledge of the same, the department possesses well-equipped laboratories. Technical event like PRAYAS: project competition are conducted to help students apply their learning in real time. Industrial visit are organized to provide much required industrial and real life exposure.

The Department of Science and Humanities lays emphasis on increasing the knowledge of the student, enhancing the critical thinking and developing the analytical thinking as way of understanding not just theory lessons but also challenges in life.

We are confident that our Engineers will emerge as assets not only to this institution and to the organization that they belong to, but also to the nation at large.

Dr. Poonam Hemnani
HOD - BSH Dept., SFIT

Department Highlights

- Academic excellence: Among top 3 Colleges of Mumbai University based on First Year result.
- In F.E. examination held in May/June 2017, the result was 77.45%.
- Academic Orientation program: glimpse of academic and co-curricular activities to Students and Parents.
- Highly qualified faculty with a passion for teaching.
- Well established teaching learning process and well-defined testing and evaluation system to ensure excellent academic performance.
- Various Student development programmes such as: Presentation competition, Value education sessions, Seminars, workshops, industrial visits etc.
- PRAYAS: Project exhibition exclusively for first year students to enhance their skills.

Research Guides/Scholars

Name of Faculty	Designation	Area of Specialisation	Teaching/ Industry Experience
Dr. Sincy George	Principal	Power system and power electronics	29
Dr. C. Hari Prasad	Professor	Snowmelt Runoff Modelling, GIS and RS applications to Environmental Engineering	21
Dr. Poonam Hemnani	Associate Professor and HOD	Organic Chemistry	13
Mr. Sunil Ramnath Pansare (Pursuing Ph. D.)	Assistant Professor	Mechanical Design Engineering, Mechanical Vibrations, Structural Health Monitoring (SHM)	18
Mr. Babasaheb D. Shinde (Pursuing Ph. D.)	Assistant Professor	Nonconventional Machining, Machining of hard-to-machine materials	7

Total faculties: 34

Professor: 2

Assistant Professor: 32

Faculties with more than 15 years' experience: 5

Staff



Laboratories:

The department has 07 labs like Auto Cadd Lab, Engineering Mechanics Lab, Applied Physics Lab, Applied Chemistry Lab, etc., all of which are well furnished with sophisticated equipment.

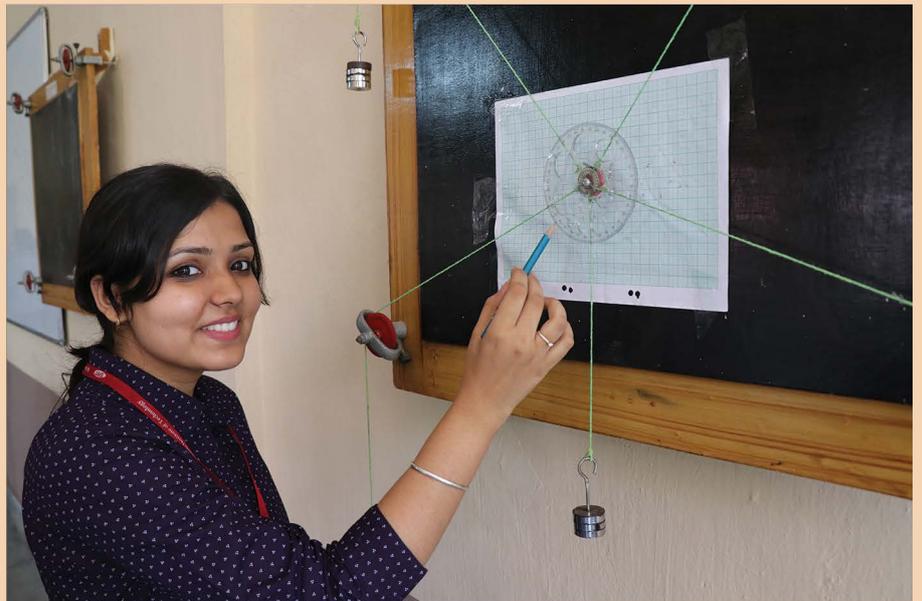


CADD Lab.

CADD systems have additional drafting features. CADD systems enable an engineer or architect to insert size annotations and other notes into a design. Through CADD students learn an important industrial art extensively used in many applications.

Engineering Mechanics Lab.

Engineering Mechanics helps to learn about statics through real life engineering examples. Engage with the theory to grasp the full understanding of simple machines and complex mathematical models.



Physics Lab.

Physics lab aids a student in establishing the relevance of the theory and gain new insights into the basic principles of physics. It aids students to develop a broad array of basic skills and tools of experimental physics and data analysis.

Language Lab.

The Language Lab provides a strong platform for practical training in language. We use the Oréll Digital Language Lab, the state-of-the-art language learning solution that increases opportunities for students to personalise learning and enhance their communication.



Workshop

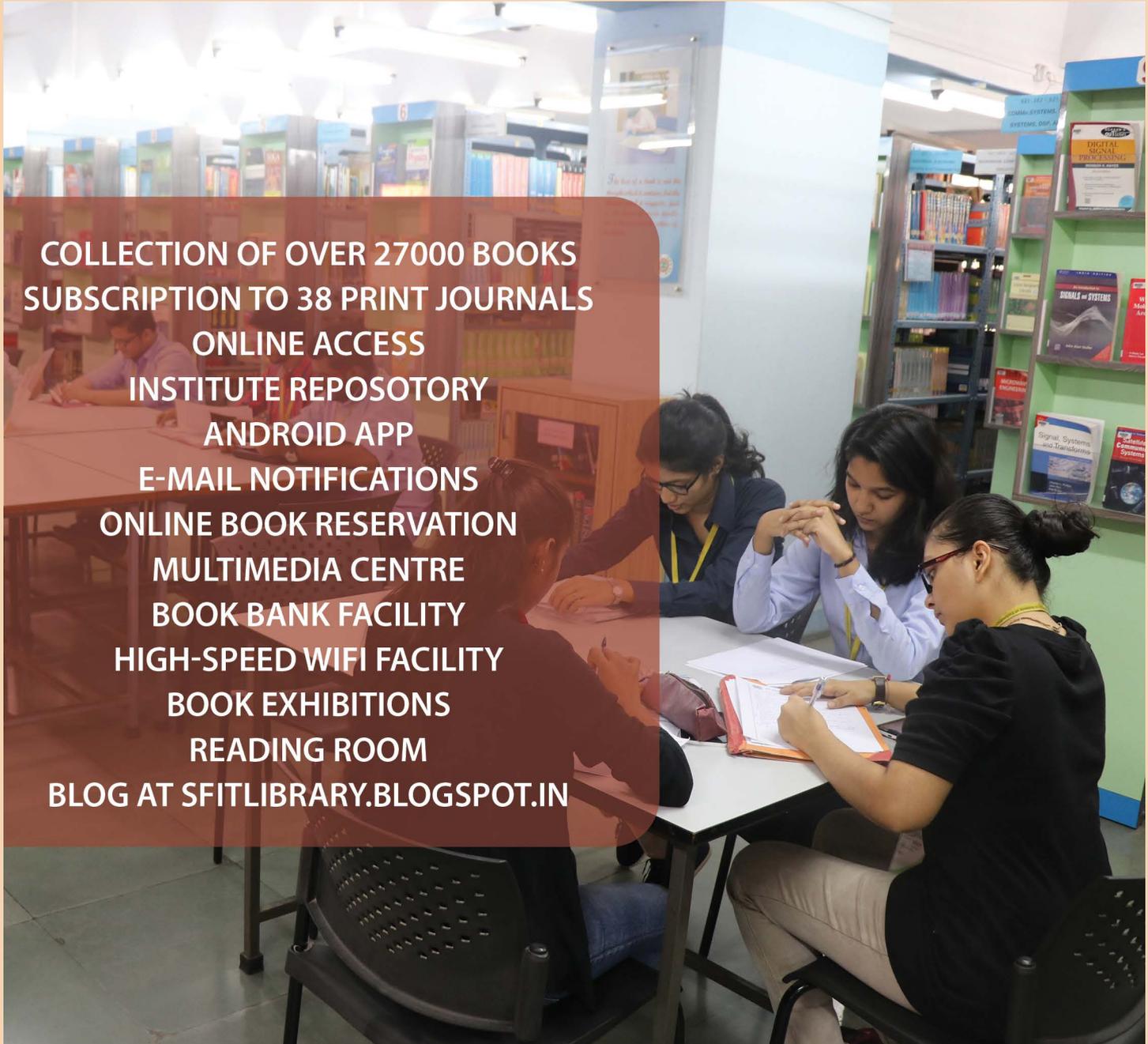
Workshop familiarises students with the basic manufacturing processes and the various tools and equipment used. Hands-on training is given in different sections, so that the students know the labour involved, machinery or equipment necessary, time required to fabricate and are able to estimate the cost of the product or job work.

Chemistry Lab.

Chemistry lab provides hands-on experience and is designed to support and illustrate chemical concepts and principles studied in the class and introduce important laboratory techniques to students and encourage analytical thinking.



Learning and Information Resource Centre (LIRC)



COLLECTION OF OVER 27000 BOOKS
SUBSCRIPTION TO 38 PRINT JOURNALS
ONLINE ACCESS
INSTITUTE REPOSITORY
ANDROID APP
E-MAIL NOTIFICATIONS
ONLINE BOOK RESERVATION
MULTIMEDIA CENTRE
BOOK BANK FACILITY
HIGH-SPEED WIFI FACILITY
BOOK EXHIBITIONS
READING ROOM
BLOG AT SFITLIBRARY.BLOGSPOT.IN



Infrastructure Facilities @ SFIT

CANTEEN



FACILITATION CENTER



BASKETBALL COURT



INDOOR GAMES



R&D CENTRE FOR STUDENTS



GYM



AUDITORIUM



Co-curricular Activities

INTERACTION WITH EXPERTS



CSCITA 2017

IEEE International Conf. on Communication Systems, Computing and IT Applications. Organized by SFIT at Hotel Kohinoor Continental, Andheri East, Mumbai.

PRAGATI

A project exhibition competition organised by E-Cell at intercollegiate level, judged by investors and industry experts.

PANEL DISCUSSION



PRIZE DISTRIBUTION



CHIEF GUEST ADDRESSES



IT FOR SOCIETY (COLLOQUIUM)

IT department organizes final-year project poster presentation, seminar/workshops for junior level involving all IT students.

MOSAIC

Annual technical event of the college.

TECHNICAL EVENTS



ORGANIZING TEAMS



ROBOCON TEAM 2017-2018



ROBOCON

Represents our college at national and international level at robotics competitions.

TECHNICAL WORKSHOP

Workshop on Deep learning and machine learning implementing image and object detection, organised by CMPN department.



HANDS-ON WORKSHOP BY B.E. STUDENT

INAUGURATION



ENTREPRENEURSHIP CELL

The E-Cell of SFIT organizes various workshops, seminars and events for promoting entrepreneurship and develop those skills in students.

PROJECT COMPETITION

First inter-college project competition, organised by the Computer Engineering Department.



PRIZE DISTRIBUTION

INAUGURATION BY THE DIRECTOR & ALUMNI



PRAYAS

A Project presentation and exhibition competition for First Year Engineering students, organized by F.E. Department.

CODE-X ACTIVITIES

Initiative by Code-X, a student chapter, for developing coding skills by teaching additional programming languages and helping them explore computer sciences.



LECTURE BY THE COORDINATOR



Extracurricular Activities



NSS
Tree plantation
Awareness drive
Blood donation
Stage painting
Visit to old-age homes
And many other social services...



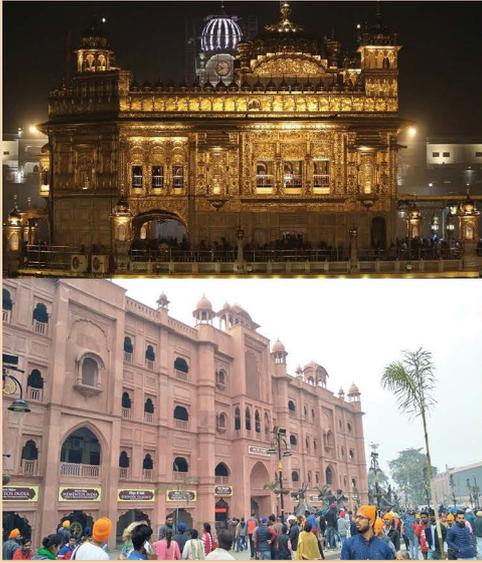
IRIS
Pre-IRIS Week
Cultural Programmes
Flash Mobs
DJ Night
Talent Hunt
And much much more..!



IGNITRA
Volleyball
Football
Cricket
Table Tennis
Athletics
And much much more..!



Industrial Visits



More Events at SFIT

DEGREE DISTRIBUTION CEREMONY



B.E. FAREWELL CEREMONY



Training & Placement Cell

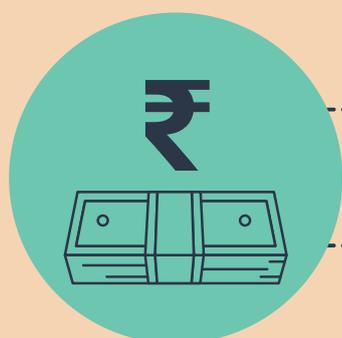
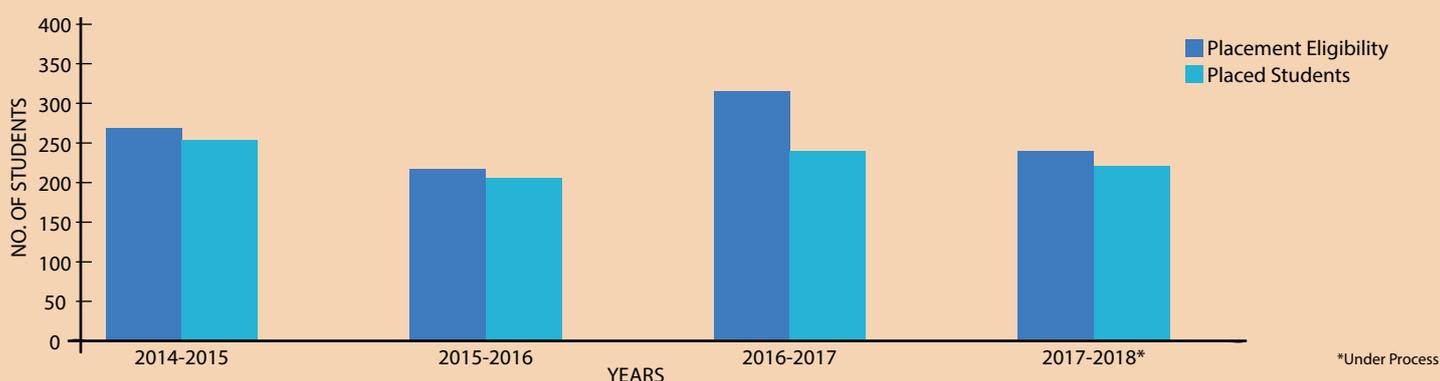


Phenomenal developments have been occurring in the field of Computer Science, Information Technology, and Telecommunication. These rapid and sometimes complex changes offer both opportunities and challenges to Industry and Academia. This will lead to large-scale changes in the job opportunities. With advances in artificial intelligence, data analytics, automation, some existent jobs will be taken over by machine but at the same time new jobs will emerge which will require completely different kind of skills. The new jobs will require higher order of critical thinking, problem solving and analytical mind-set to succeed in the new environment. We at SFIT are committed to creating engineers who understand the industry needs and will be an asset to organizations and nation at large. SFIT foresees these changes and ensures to take measures to equip students with necessary technical and behavioral competencies. This is achieved through our rigorous and meticulously developed academic curriculum and bringing the proper alignment with intense Industry Institute Interaction. In addition to this, we encourage our students to become industry ready professionals through proactive participation in training towards technical certifications, participation in technical competitions, carrying out live projects, seminar presentations on latest topics, community services to give a sense of social awareness and cultural events for showcasing artistic skills. We plan to successfully mentor the young student into a professional through the 4 year of engineering program.

Mr. Wilson Pinto

Head - Training & Placement, SFIT

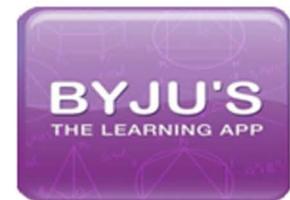
Placement Records



HIGHEST PACKAGE: 10 Lac per annum

AVERAGE PACKAGE: 3.3 Lac per annum

Our Few Recruiters



Ongoing Training & Placement Programmes

The Placement department in consultation with college management organises various training program to equip the students to be Industry ready. We have collaboration with Global Talent Track (GTT) and Barclays to impart Soft Skill training to our students. Similiarly, we have tie-up with Infosys for technical training. The college Alumni takes active interest in interacting with their juniors to help them understand the latest requirements in Industry and also help in placements.

Following trainings are organised:

1. Programming Principles (using Java or Dot Net)
2. Advance Programming
3. Data Science (Artificial Intelligence and Machine learning)
4. Big Data Analytics
5. Cloud Computing
6. Information Security
7. SAP-ABAP
8. Advance Database using Oracle
9. Azure
10. Amazon Web Services(AWS)

Distinguished Alumni of SFIT



MR. KRISHNADAS WARRIER
MBA, Lancaster University, UK.

MR. BENSON NINAN
UPSC rank 556



DR. NICOLLE CORREA
MS and Ph.D.,
Electrical engineering,
University of Maryland,
Baltimore.



MS. PADMINI PATNAIK
Executive Director,
Morgan Stanley.



MR. AUSTIN DCOSTA
Senior Solutions Architect,
MOH Holdings Pte Ltd,
Singapore

MR. VIKRAM BHAT
MS, San Jose State University.
Software Engineer,
Sony PlayStation.



Our Guest Speakers

• INTERNATIONAL

Dr. Anthony A. Maciejewski
Professor and Head
Department of Electrical and
Computer Engineering
Colorado State University, U.S.A.

Dr. Marylou G. Dewald
Dean, Global Initiatives
Professor, Angell Snyder
School of Business
Ottawa University

Ms. Kimberly Rieken
Assistant Director of
International Programs
Ottawa University

Mr. Frank Trocki
Sr. Partner
Howell Management Services
Marshfield, MA

Dr. Raj Kettimutthu
University of Chicago, U.S.A.

• NATIONAL

Dr. Sunil Kumar Kopparapu
Senior Scientist
Tata Consultancy Services Limited

Dr. M. A. Atmanand
Scientist 'G' & Former Director
National Institute of Ocean Technology

Dr. Satyanarayana Bheesette
Scientific Officer (H)
Tata Institute of Fundamental Research

Mr. Gigi Joseph
Chief Scientific Officer
Bhabha Atomic Research Centre

Mr. Thomas Mathew
President, Reliance Technology Group
Reliance Industries Limited, Mumbai

Mr. Chinmay D. Gavankar
Country Manager,
IBM Cloud Services

Mr. N. V. Joseph
Deputy Chief Signal & Telecom
Department, Indian Railways

Mr. Aditya Patil
Vice President, Morgan Stanley

Mr. Albert W. D'souza
Chairman,
Aldel Education Trust

Dr. Amit Dutta
Regional Officer,
WRO AICTE

Dr. Supratim Biswas
Professor,
Indian Institute of Technology, Bombay

Dr. Vivek Agarwal
Professor,
Indian Institute of Technology, Bombay

Dr. T. S. Rathore
Ex-Professor,
Indian Institute of Technology, Bombay

Dr. R. D. Kulkarni
Professor and Director
University Institute of Chemical
Technology, Jalgaon

Fr. John Rose S.J.
Director,
Xavier Institute of Engineering, Mahim,
Mumbai

Admission Procedure for 2-year full-time M.E. Courses AY 2018-2019

1. Admission procedure in general

1.1 Minimum eligibility criteria for applying for admission to M.E. Courses are as prescribed by the Directorate of Technical Education, Maharashtra State, Mumbai. For details kindly refer to D.T.E. website: www.dtemaharashtra.gov.in OR www.mahacet.org and DTE Information Brochure AY 2018-19.

1.2 The qualifying examination for M.E. admissions for the academic year 2018-2019 is positive score in GATE 2018 conducted by IIT in respective qualifying branch.

1.3 Distribution of seats under Minority, Institute level and CAP quota are as notified by the Directorate of Technical Education (DTE), Maharashtra State, Mumbai.

1.4 For details of admission procedure under Centralized Admission Process (CAP), including schedule of activities, submission of Application Form & required documents etc., kindly refer to DTE website (i.e., www.dtemaharashtra.org.in).

1.5 Minority admission procedure: For details of admission procedure under Minority quota, kindly refer to DTE website (i.e., www.dtemaharashtra.org.in).

1.6 Admission procedure for institute level/CAP vacancy seats: For admission for institute level/CAP vacancy seats, candidates are required to fill in an Application Form and submit online on the college website: www.sfitengg.org, under 'Admissions'. A printout of the same Application Form duly signed is to be submitted in person to the college office, along with one set of attested copies of needed documents for verification and a processing fee of ₹1200/- (payable in cash) to the college office during working hours: 10 a.m. to 4 p.m. (Monday to Friday). Details of the admission procedure/schedule shall be notified on the college notice board/website (i.e., www.sfitengg.org).

1.7 Documents to be submitted by all categories along with Application Form: The application form must be accompanied by one set of attested true copies of the following documents for all admissions to M.E. Course:

- GATE Score Card mentioning validity period and qualified/non-zero score out of 100 appeared in 2018 or as per DTE Eligibility Criteria.
- Bachelor's Degree in Engineering/ Technology Certificate and Mark Sheets (all semesters).
- Secondary School Certificate (SSC – Std. X) and Higher Secondary Certificate (HSC – Std. XII/ ICSE/ CBSE) Mark Sheet & Certificate as the case may be.
- Leaving Certificate from the Institute last attended.
- Proof of Indian Nationality (Birth/ Domicile Certificate).
- Transfer and Migration Certificate (in case of candidates who have passed qualifying examination of BE/ BTech from other than University of Mumbai).
- Physical/ Medical Fitness Certificate from a Registered Medical Practitioner.

- FC acknowledgement copy (Given at the time of online document verification)
- Pre-admission online registration on University of Mumbai Digital University Portal (<http://mum.digitaluniversity.ac>)

Important note: All candidates seeking admission to Engineering are compulsorily required to register with the University of Mumbai for “Pre-admission” on University of Mumbai Digital University Portal (<http://mum.digitaluniversity.ac>) and Directorate of Technical Education’s website (i.e., www.dte.org.in)

Additionally, for students seeking admission for Roman Catholic/Christian Minority Seats:-

- Proof of Roman Catholic/Christian Status
- Candidates’ College/School Leaving Certificate with Religion mentioned as “Roman Catholic/Christian”.

OR

- School/College Leaving Certificate of Candidate’s Father/Brother/Unmarried Sister with Religion mentioned as “Roman Catholic/Christian”.

OR

- An Affidavit on a ₹100/- stamp paper & Undertaking (in college format) stating that he/she is a Roman Catholic/ Christian accompanied by (a) Baptism Certificate and (b) Letter from the Parish Priest.

- All Minority candidates should have passed X Std (SSC) and XII Std (HSC) from Maharashtra State.

Note: The initial admission notification for institute level/CAP vacancy seats is issued in the local newspapers, Mumbai Edition (English and Marathi) and also is displayed on the College Notice Board as well as website: www.sfitengg.org

1.8 Change of Marks due to Verification

- If marks in GATE 2018 and HSC examination are modified due to verification and the same is duly certified by the Competent Authority, it shall be reported to the Principal of the Institute immediately.
- The effect of such change will be taken into consideration only for the subsequent rounds of admission.

2. College fees

The Fee for students to be admitted to the course in the academic year 2018-2019 is as fixed by the Fees Regulatory Authority appointed by the Government of Maharashtra. The approved Fee for the Academic Year 2018 -2019 is as given below:

Tuition fee & Development fee	₹ 1,16,500
University/Exam/Other fees	₹ 8,425
Total fees	₹ 1,24,925

3. Refund of tuition, development and other fees

The rules for refund of fees for all categories are as notified by Directorate of Technical Education, Mumbai, Maharashtra State. (Refer DTE Brochure)

4. Admission schedule

Detailed Admission Procedure/Schedule etc. shall be displayed on the college notice board/college website for institute level & CAP vacancy seats. No communication shall be sent to the individual candidates. It is the responsibility of the candidates to see the college notice board and present themselves for counselling rounds along with parent/guardian and all the necessary documents as well as college fees of ₹ 1,24,925/- (Tuition + University fees) by pay order/demand draft at the scheduled time and date specified. Failure to pay the prescribed fee at the time of counselling/admission shall entail loss of candidature.

5. M.E. 2nd Year (Semester III & IV)

As per the University of Mumbai, the Post Graduate M.E. program is a 2-year full time course and as such students studying in the 2nd Year (SEM-III & SEM - IV) are required to attend the college to work on their assignments, seminars, project etc. During this period, they will also be given additional inputs, like pedagogical skills, e-learning, ICT and Research Methodology (30 – 40 hours duration).

The College does not permit its II year students to undertake any assignments or jobs outside during the entire term of the course (24 months).

6. General instructions

6.1 Attendance and discipline

- Every student must complete, to the satisfaction of the Principal, the courses of study at the college prescribed for such term for the class to which the student belongs. The student must keep a minimum attendance of three-fourth (75%) of the number of days on which lectures and practicals are held in each term.
- There will be an examination at the end of each semester. The examinations at the end of each semesters I, II, III and IV are conducted by the University.
- The SFIT Management along with parental involvement is committed to a healthy discipline and high academic standards and the students are required to co-operate to maintain the same.
- From placement point of view, today more and more organizations value discipline as one of the critical factor over and above academic performance. Both students and parents are requested to take note of this aspect and co-operate with the college authorities, in the interest of their wards for better placements.

6.2 Prohibition and prevention of ragging

As per the directions of the Honourable Supreme Court in SLP No. 24295 of 2006 dated 16-05-2007 and in Civil Appeal No. 887 of 2009 dated 08-05-2009 to prohibit, prevent and eliminate the scourge of ragging, as well as subsequent notifications issued by AICTE, University of Mumbai, State Government/Directorate of Technical Education and Central Government,

ragging is strictly prohibited.

Further, the All India Council for Technical Education (AICTE) has notified regulation for prevention and prohibition of ragging in AICTE approved technical Institutions vide No.37-3/Legal/AICTE/2009 dated 01/07/2009 available on AICTE web portal. Accordingly, all candidates seeking admission in SFIT are required to submit an 'Affidavit' in the prescribed format duly signed by the candidates and parent/guardian to the effect that they are aware of the law regarding prohibition of ragging as well as the punishments, and that he/she, if found guilty of the offence of ragging, is liable to be punished appropriately, which could even include expulsion from the college.

6.3 Grievance redressal committee

As per AICTE regulations, the college has constituted 'Grievance Redressal Committee' consisting of senior staff members. The aggrieved party may approach the Committee for the redressal of grievance by giving in writing the grievance to Office Executive (Admission and Student Affairs.) The grievance shall be discussed in the Committee meetings and the Chairman shall communicate its decision in writing to the concerned party.

6.4 Internal complaints committee

Complaints from female students including matters related to sexual harassment are handled by 'Internal Complaints Committee'

6.5 Mobile phones banned in the college premises

As per Mumbai University's circular ref. no. UG/552 of 2004 dated 31st December 2004; mobile phones in the college premises are banned. Strict action shall be taken against students found violating this rule by confiscating their mobile phones and imposing a fine.

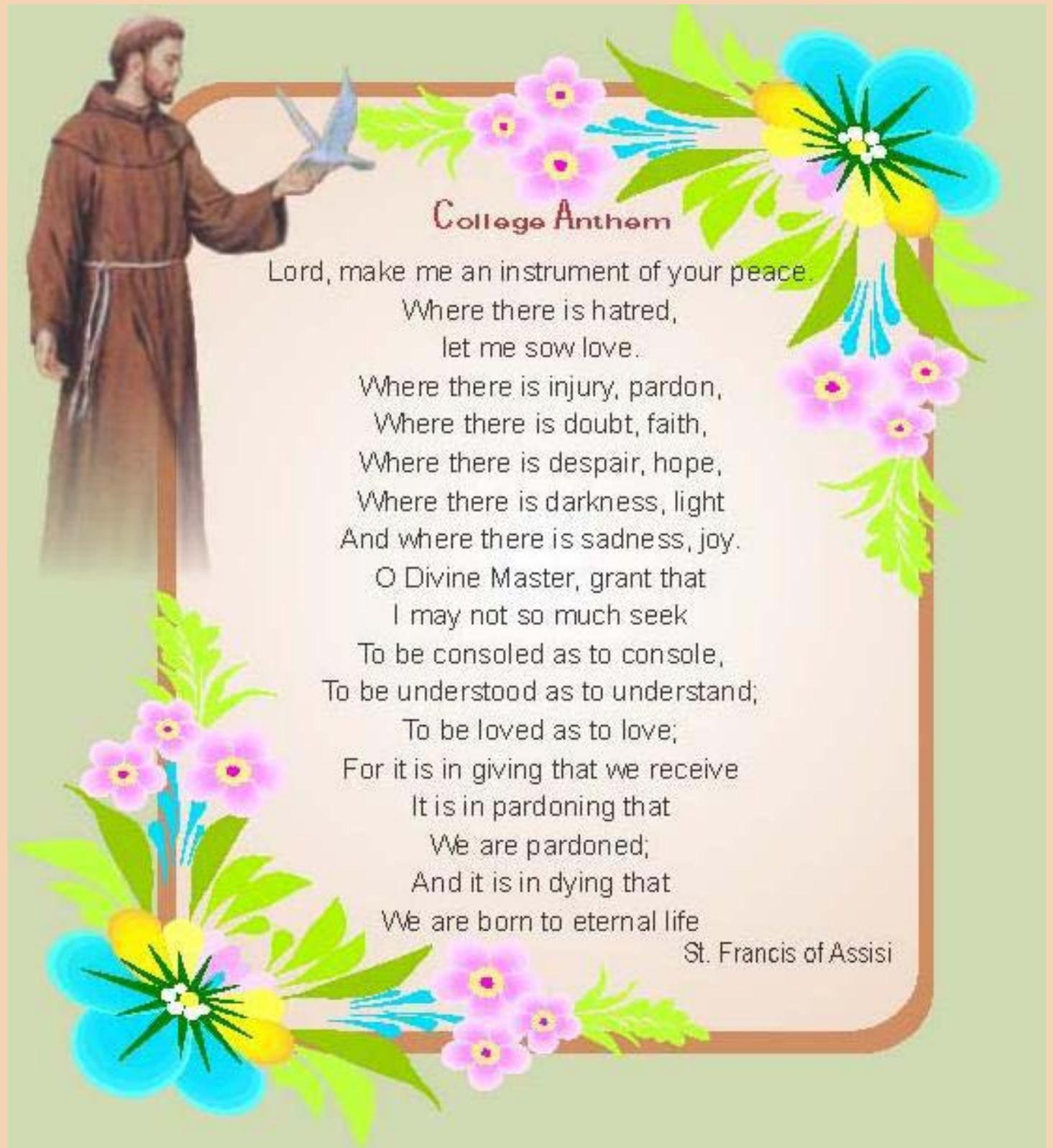
6.6 Anti-national activities

Students while studying in any Engineering/Technology Institute, if found indulging in anti-national activities contrary to the provisions of acts and laws enforced by Government from time to time, any activity contrary to rules of discipline are liable to be expelled from the Institute without any notice, by the Principal/Director of the Institute.

6.7 False or incorrect information

If any statement(s) made in the Application Form or any information supplied by the candidate in connection with his/her admission is found to be false or incorrect at any time, then his/her admission will be cancelled, fees forfeited and he/she may be expelled from the college and prosecuted, if deemed necessary. (An appeal against the order of expulsion, however, may be referred to the College Academic Council, whose decision in such cases will be final and binding)

6.8 Kindly visit college website for Mandatory Disclosure.



Location Map

